**News Release**

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**Renesas Unveils Powerful Single-Chip RZ/V2H MPU for Next-Gen Robotics with Vision AI and Real-Time Control**

*New Generation AI Accelerator with 10 TOPS/W Power Efficiency Delivers AI Inference Performance of up to 80 TOPS Without Cooling Fan*

**Düsseldorf, February 29, 2024 ―** Renesas Electronics Corporation (TSE:6723), a premier supplier of advanced semiconductor solutions, has expanded its popular RZ Family of microprocessors (MPUs) with a new device targeting high-performance robotics applications.

Offering the highest levels of performance within the family, the RZ/V2H enables both vision AI and real-time control capabilities.

The device comes with a new generation of Renesas proprietary AI accelerator, DRP (Dynamically Reconfigurable Processor)-AI3, delivering 10 TOPS/W power efficiency, an impressive 10-fold improvement over previous models. Additionally, pruning technology employed in the DRP-AI3 accelerator significantly improves AI computing efficiency, boosting AI inference performance up to 80 TOPS. This performance boost allows engineers to process vision AI applications directly at edge AI devices without relying on cloud computing platforms. The details of the new [DRP-AI3 acceleration technology](https://www.renesas.com/about/press-room/renesas-develops-new-ai-accelerator-lightweight-ai-models-and-embedded-processor-technology-enable) were recently presented at the International Solid-State Circuits Conference (ISSCC 2024) in San Francisco.

The RZ/V2H incorporates four Arm® Cortex®-A55 CPU cores with a maximum operating frequency of 1.8 GHz for Linux application processing, two Cortex-R8 cores running at 800 MHz for high-performance real-time processing, and one Cortex-M33 as a sub core. By integrating these cores into a single chip, the device can effectively manage both vision AI and real-time control tasks, making it ideal for demanding robotics applications of the future. Since the RZ/V2H consumes less power, it eliminates the need for cooling fans and other heat-dissipating components. This means engineers can design systems that are smaller in size, less expensive, and more reliable.

“As a market leader in motor control microprocessors, Renesas is ready to take on the next challenge to drive the advancement of the robotics market with AI technology,” said **Daryl Khoo, Vice President of the Embedded Processing 1st Division at Renesas**. “The RZ/V2H will facilitate the development of next-generation autonomous robots with vision AI capabilities, that have the ability to think independently and control movements in real time."

Renesas has applied its proprietary [DRP technology](https://www.renesas.com/key-technologies/artificial-intelligence/drp) to develop the [OpenCV Accelerator](https://www.renesas.com/software-tool/opencv-accelerator) that speeds up the processing of OpenCV, an open-source industry standard library for computer vision processing. The resulting speed improvement is up to 16 times faster compared to CPU processing. The combination of the DRP-AI3 and the OpenCV Accelerator enhances both AI computing and image processing algorithms, enabling the power-efficient, real-time execution of Visual SLAM(Note 1) used in applications such as robot vacuum cleaners.

To accelerate development, Renesas also released AI Applications, a library of pre-trained models for various use cases, and the AI SDK (Software Development Kit) for rapid development of AI applications. By running these software on the RZ/V2H's evaluation board, engineers can evaluate AI applications easily and earlier in the design process, even if they do not have extensive knowledge of AI.

“We are thrilled to be part of the launch of the RZ/V2H, which combines AI technology with real-time control,” says **Rolf Segger, founder of SEGGER Microcontroller GmbH**. “SEGGER’s J-Link debug probe, widely adopted by numerous embedded development projects globally, will provide the support needed for the RZ/V2H, helping accelerate the development of next-generation robotic innovations. We look forward to this next phase in our multi-decade long partnership with Renesas."

**Winning Combinations**Renesas has developed the "[Visual Detection Single Board Computer](https://www.renesas.com/applications/industrial/industrial-automation/visual-detection-single-board-computer?utm_campaign=f-up-mpu_rzv2h-epsg-epbd-epbz-null&utm_source=null&utm_medium=pr&utm_content=wc)" that uses camera images to identify its surroundings, and to determine and control its movements in real-time. This solution combines the RZ/V2H with power management ICs and VersaClock programmable clock generators to support power-efficient industrial robots and machinery. Its efficient design eliminates the requirement for an additional cooling fan, keeping the solution BOM and size down. These Winning Combinations are technically vetted system architectures from mutually compatible devices that work together seamlessly to bring an optimized, low-risk design for faster time to market. Renesas offers more than 400 Winning Combinations with a wide range of products from the Renesas portfolio to enable customers to speed up the design process and bring their products to market more quickly. They can be found at [renesas.com/win](https://www.renesas.com/win).

**Availability**

The RZ/V2H is available today, along with the evaluation board and the AI SDK. More information about the device and development tools are available at: [https://www.renesas.com/rzv2h.](https://www.renesas.com/products/microcontrollers-microprocessors/rz-mpus/rzv2h-quad-core-vision-ai-mpu-drp-ai3-accelerator-and-high-performance-real-time-processor?utm_campaign=f-up-mpu_rzv2h-epsg-epbd-epbz-null&utm_source=null&utm_medium=pr&utm_content=pp)

**About Renesas Electronics Corporation**

Renesas Electronics Corporation ([TSE: 6723](http://www.jpx.co.jp/english/)) empowers a safer, smarter and more sustainable future where technology helps make our lives easier. The leading global provider of microcontrollers, Renesas combines our expertise in embedded processing, analog, power and connectivity to deliver complete semiconductor solutions. These Winning Combinations accelerate time to market for automotive, industrial, infrastructure and IoT applications, enabling billions of connected, intelligent devices that enhance the way people work and live. Learn more at [renesas.com](http://www.renesas.com/). Follow us on [LinkedIn](https://www.linkedin.com/company/renesas/), [Facebook](https://www.facebook.com/RenesasElectronics/), [X](https://twitter.com/renesasglobal), [YouTube](https://www.youtube.com/user/RenesasPresents), and [Instagram](https://www.instagram.com/renesas_global/).

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(Note 1)Visual SLAM (Simultaneous Localization and Mapping) is a technology that analyzes images captured by on-board cameras on robots and drones and estimates their own position while simultaneously creating detailed maps of their surroundings.

(Remarks) This DRP-AI technology uses a part of the results of work commissioned by the New Energy and Industrial Technology Development Organization (NEDO). Arm, Arm Cortex are trademarks or registered trademarks of Arm Limited in the EU and other countries. All names of products or services mentioned in this press release are trademarks or registered trademarks of their respective owners.

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